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181 7590 05/28/2004

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| (Depositor's name) |
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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-----------------|-------------|----------------------|---------------------|------------------|
| 10/614,789 | 07/09/2003 | Kazuyuki Tokorozuki | XA-9898 | 8544 |

TITLE OF INVENTION: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

| APPLN. TYPE | SMALL ENTITY | ISSUE FEE | PUBLICATION FEE | TOTAL FEE(S) DUE | DATE DUE |
|----------------|--------------|-----------|-----------------|------------------|------------|
| nonprovisional | NO | \$1330 | \$300 | \$1630 | 08/30/2004 |

| EXAMINER | ART UNIT | CLASS-SUBCLASS |
|-----------|----------|----------------|
| VU, DAVID | 2818 | 430-005000 |

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Miles & Stockbridge P.C.

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Renesas Technology Corp.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

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(Authorized Signature)  (Date) 8/9/04
Mitchell W. Shapiro, Reg. No. 317368

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